

<b>Notic of References Cited</b>	Application/Control No. 09/437,135	Applicant(s)/Patent Under Reexamination YAMAZAKI ET AL.
	Examiner Erik Kielin	Art Unit 2813

**U.S. PATENT DOCUMENTS**

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	A	US-4,996,575	02-1991	Ipri et al.	257/369
	B	US-5,116,771	05-1992	Karulkar, Pramod C.	257/369
	C	US-5,274,279	12-1993	Misawa et al.	326/103
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	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, et al. Silicon Processing for the VLSI Era, Vol. 1-Process Technology, Lattice Press: Sunset Beach CA, 1986, pp. 183-184.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.